

625/579

Class	Subclass

**ISSUE CLASSIFICATION**

PATENT NUMBER

**U.S. UTILITY Patent Application**

**O.P.E.**

PATENT DATE

SCANNED

Illness 3 as 17

APPLICATION NO. 09/987012	CONT/PRIOR F	CLASS 257	SUBCLASS 788	ART UNIT <del>2815</del> 2815	EXAMINER <del>Elliott</del> Elliott
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Kenichi Watanabe

Chun

Semiconductor wafer device having separated conductive patterns in peripheral area and its manufacture method

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			_____ (Primary Examiner) (Date)	
<b>ISSUE FEE</b>				
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